



# N-Channel 220-V (D-S) MOSFET

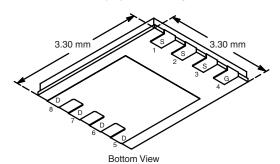
| PRODUCT SUMMARY     |                                 |                                 |                       |  |
|---------------------|---------------------------------|---------------------------------|-----------------------|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$            | I <sub>D</sub> (A) <sup>a</sup> | Q <sub>g</sub> (Typ.) |  |
| 220                 | 0.320 at V <sub>GS</sub> = 10 V | 8.4                             | 9.1 nC                |  |
|                     | 0.340 at V <sub>GS</sub> = 6 V  | 8.2                             | 9.1110                |  |

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested

# Pb-free ROHS COMPLIANT HALOGEN FREE

#### PowerPAK 1212-8

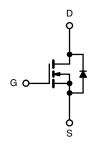


Ordering Information: Si7302DN-T1-E3 (Lead (Pb)-free)

Si7302DN-T1-GE3 (Lead (Pb)-free and Halogen-free)

#### **APPLICATIONS**

· Primary Side Switching



N-Channel MOSFET

| <b>ABSOLUTE MAXIMUM RATINGS</b>                     | T <sub>A</sub> = 25 °C, unles     | ss otherwise not     | ted                 |      |  |
|---|-----------------------------------|----------------------|---------------------|------|--|
| Parameter   |                                   | Symbol               | Limit               | Unit |  |
| Drain-Source Voltage                                | $V_{DS}$                          | 220                  | V                   |      |  |
| Gate-Source Voltage                                 |                                   | V <sub>GS</sub> ± 20 |                     | v    |  |
|   | T <sub>C</sub> = 25 °C            |                      | 8.4                 |      |  |
| Continuous Drain Current (T <sub>.I</sub> = 150 °C) | T <sub>C</sub> = 70 °C            | I-                   | 6.7                 |      |  |
| Continuous Diam Current (1) = 130 C)                | T <sub>A</sub> = 25 °C            | I <sub>D</sub>       | 2.3 <sup>b, c</sup> |      |  |
|   | T <sub>A</sub> = 70 °C            |                      | 1.8 <sup>b, c</sup> | Α    |  |
| Pulsed Drain Current                                |                                   | I <sub>DM</sub>      | 10                  |      |  |
| Continuous Source-Drain Diode Current               | T <sub>C</sub> = 25 °C            | I <sub>S</sub>       | 8.4                 |      |  |
| Continuous Source-Drain Diode Current               | T <sub>A</sub> = 25 °C            | 'S                   | 3.2 <sup>b, c</sup> |      |  |
|   | T <sub>C</sub> = 25 °C            |                      | 52                  |      |  |
| Maximum Power Dissipation                           | T <sub>C</sub> = 70 °C            | P <sub>D</sub>       | 33                  | w    |  |
| Maximum Fower Dissipation                           | T <sub>A</sub> = 25 °C            | ' D                  | 3.8 <sup>b, c</sup> |      |  |
|   | T <sub>A</sub> = 70 °C            |                      | 2.0 <sup>b, c</sup> |      |  |
| Operating Junction and Storage Temperature Ra       | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150          | °C                  |      |  |
| Soldering Recommendations (Peak Temperature         | _                                 | 260                  |                     |      |  |

| THERMAL RESISTANCE RATINGS                  |              |            |         |         |      |  |
|---|--------------|------------|---------|---------|------|--|
| Parameter                                   |              | Symbol     | Typical | Maximum | Unit |  |
| Maximum Junction-to-Ambient <sup>b, f</sup> |              | $R_{thJA}$ | 26      | 33      | °C/W |  |
| Maximum Junction-to-Case (Drain)            | Steady State | $R_{thJC}$ | 1.9     | 2.4     | ]    |  |

#### Notas.

- a. Based on  $T_C = 25$  °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. See Solder Profile (<a href="www.vishay.com/ppg?73257">www.vishay.com/ppg?73257</a>). The PowerPAK 1212-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 81  $^{\circ}\text{C/W}.$

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| Static  | Symbol                           | rest Conditions   | WIII. | Тур.  | Max.  | Unit     |  |  |
|---|----------------------------------|---|-------|-------|-------|----------|--|--|
| Drain-Source Breakdown Voltage                | V <sub>DS</sub>                  | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$   | 220   |       |       | l v      |  |  |
| V <sub>DS</sub> Temperature Coefficient       | ΔV <sub>DS</sub> /T <sub>J</sub> | VGS = 0 V, 1D = 230 μ/ (  | 220   | 240   |       | · •      |  |  |
| V <sub>GS(th)</sub> Temperature Coefficient   |                                  | I <sub>D</sub> = 250 μA   |       |       |       | mV/°C    |  |  |
| · /   | $\Delta V_{GS(th)}/T_J$          | $V_{DS} = V_{GS}, I_D = 250 \mu A$  |       | 7.7   | 4     | V        |  |  |
| Gate-Source Threshold Voltage                 | V <sub>GS(th)</sub>              |   | 2     |       | 4     |          |  |  |
| Gate-Source Leakage                           | I <sub>GSS</sub>                 | $V_{DS} = 0 \text{ V, } V_{GS} = 20 \text{ V}_{GS}$                                   |       |       | 100   | nA       |  |  |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>                 | V <sub>DS</sub> = 220 V, V <sub>GS</sub> = 0 V  |       |       | 1     | μΑ       |  |  |
| <u> </u>                                      |                                  | V <sub>DS</sub> = 220 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C                |       |       | 10    |          |  |  |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>               | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$                                       | 10    |       |       | Α        |  |  |
| Drain-Source On-State Resistance <sup>a</sup> | R <sub>DS(on)</sub>              | $V_{GS} = 10 \text{ V}, I_D = 2.3 \text{ A}$  |       | 0.260 | 0.320 | Ω        |  |  |
| Diam-Source On-State Hesistance               | 1.03(011)                        | $V_{GS} = 6 \text{ V}, I_D = 2.2 \text{ A}$   |       | 0.280 | 0.340 |          |  |  |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>                  | $V_{DS} = 15 \text{ V}, I_{D} = 2.3 \text{ A}$  |       | 11    |       | S        |  |  |
| Dynamic <sup>b</sup>                          |                                  |   |       |       |       |          |  |  |
| Input Capacitance                             | C <sub>iss</sub>                 |   |       | 645   |       | pF       |  |  |
| Output Capacitance                            | C <sub>oss</sub>                 | V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz                              |       | 72    |       |          |  |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>                 |   |       | 47    |       |          |  |  |
| T. 10 1 01                                    | Q <sub>g</sub>                   | V <sub>DS</sub> = 110 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.3 A               |       | 14    | 21    |          |  |  |
| Total Gate Charge                             |                                  | V <sub>DS</sub> = 110 V, V <sub>GS</sub> = 6 V, I <sub>D</sub> = 2.3 A                |       | 9.1   | 14    | nC       |  |  |
| Gate-Source Charge                            | $Q_{gs}$                         |   |       | 2.8   |       |          |  |  |
| Gate-Drain Charge                             | Q <sub>gd</sub>                  |   |       | 4.2   |       |          |  |  |
| Gate Resistance                               | R <sub>q</sub>                   | V <sub>GS</sub> = 0.1 mV, f = 1 MHz   | 0.9   | 1.8   | 2.7   | Ω        |  |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>               |   |       | 10    | 15    |          |  |  |
| Rise Time                                     | t <sub>r</sub>                   | $V_{DD} = 110 \text{ V}, R_1 = 110 \Omega$  |       | 10    | 15    | ns       |  |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub>              | $I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_q = 6 \Omega$                      |       | 20    | 30    |          |  |  |
| Fall Time                                     | t <sub>f</sub>                   | j   |       | 15    | 25    |          |  |  |
| Drain-Source Body Diode Characteristi         |                                  | C. unless otherwise noted   |       |       |       | <u> </u> |  |  |
| Continuous Source-Drain Diode Current         | I <sub>S</sub>                   |   |       |       | 8.4   | A        |  |  |
| Pulse Diode Forward Current                   | I <sub>SM</sub>                  |   |       |       | 10    |          |  |  |
| Body Diode Voltage                            | V <sub>SD</sub>                  | I <sub>S</sub> = 3.2 A, V <sub>GS</sub> = 0 V   |       | 0.8   | 1.2   | V        |  |  |
| Body Diode Reverse Recovery Time              | t <sub>rr</sub>                  | 13, 133 - 1   |       | 65    | 100   | ns       |  |  |
| Body Diode Reverse Recovery Charge            | Q <sub>rr</sub>                  | -   |       | 163   | 250   | nC       |  |  |
| Reverse Recovery Fall Time                    | t <sub>a</sub>                   | $I_F = 3.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$ |       | 45    | 250   | ns       |  |  |
|   |                                  |   |       | _     |       |          |  |  |
| Reverse Recovery Rise Time                    | t <sub>b</sub>                   |   |       | 20    |       |          |  |  |

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

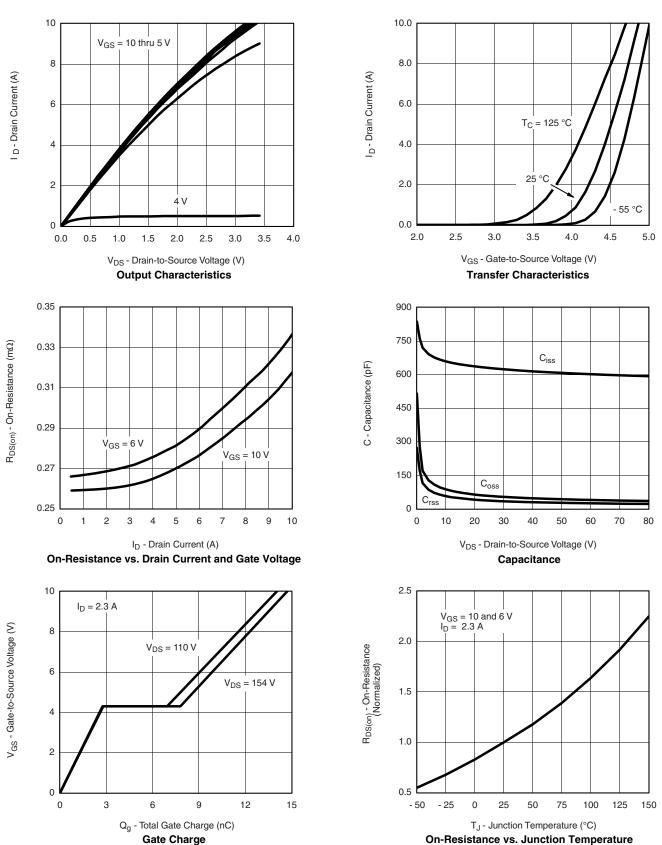
b. Guaranteed by design, not subject to production testing.







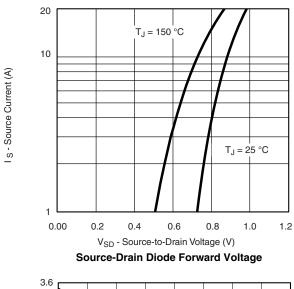
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

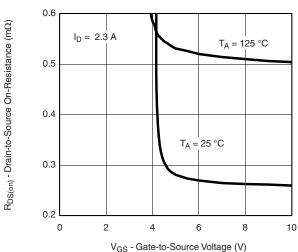


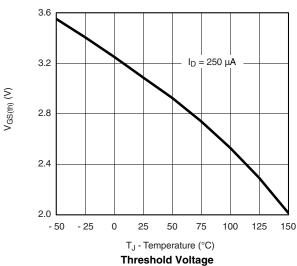
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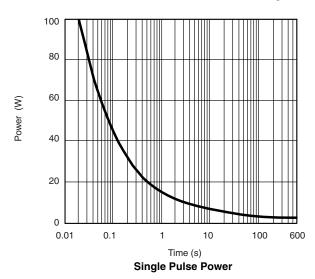
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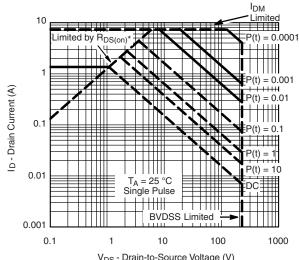






On-Resistance vs. Gate-to-Source Voltage





 $V_{DS} \text{ - Drain-to-Source Voltage (V)} \\ ^*V_{GS} \text{ > minimum V}_{GS} \text{ at which R}_{DS(on)} \text{ is specified}$ 

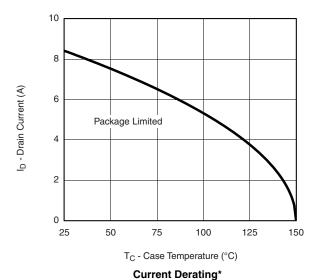
Safe Operating Area

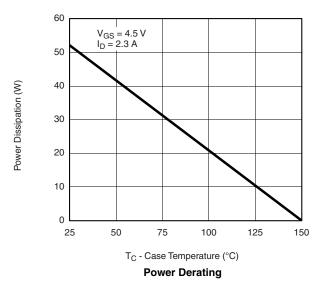






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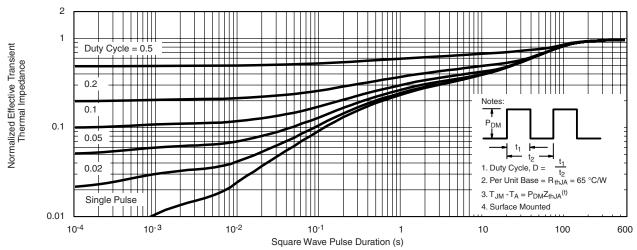
Document Number: 73306 S-83051-Rev. D, 29-Dec-08

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

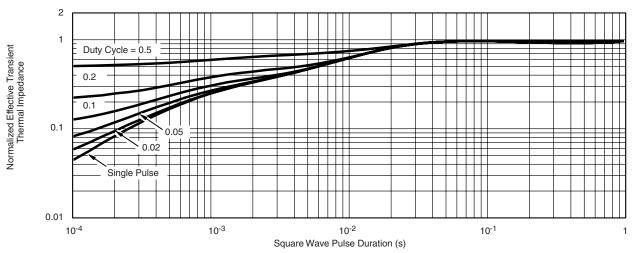
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# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?73306">www.vishay.com/ppg?73306</a>.



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